

Bernd Becker

Institut für Informatik

A close-up photograph of a yellow microchip with a black border, mounted on a dark substrate.

Lehrstuhl für
Rechnerarchitektur

A grayscale image of a complex printed circuit board (PCB) with intricate traces and components.

ACTUELLES
TEAM

FORSCHUNG
LEHRE

PROJEKTE
KONTAKT

PUBLIKATIONEN
IMPRESSUM

Mitteilungen

Synthese: FPGA, Parallelrechner, Layout

- Thomas Eschbach
- Matt Lewis
- Dr. Frank Schmiedle
- Tobias Schubert

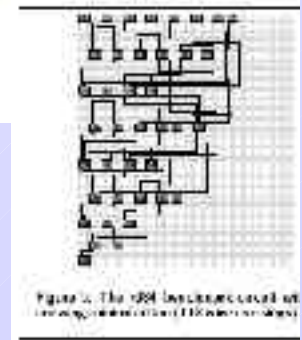
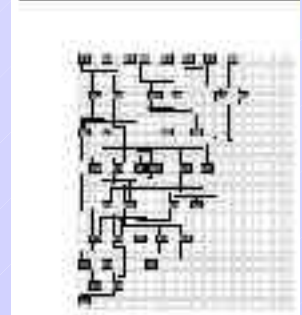


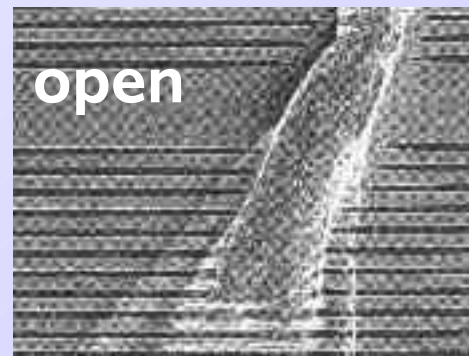
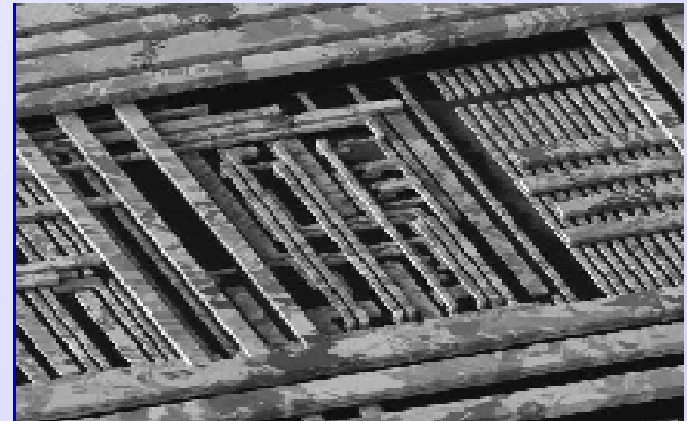
Figure 3. The 256 logic blocks of an FPGA (simplified 16x16 grid).



Test:

deep submicron, reliability, diganosis

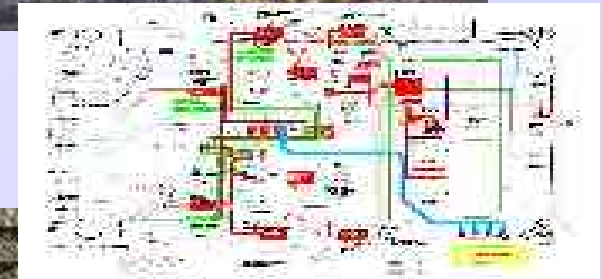
- Dr. Ilia Polian
- Piet Engelke
- Alejandro Czutro
- Thomas Fiehn
- Stefan Spinner



Zuverlässigkeit, Verifikation: *Embedded Systems*



- Erika Abraham
- Stefan Disch
- Marc Herbstritt
- Ralf Wimmer
- Jochen Eisinger
- Stefan Kupferschmid





Support

- Angelika Fabry Flashar
- Peter Winterer



Research

